

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L5	2	L4 and (map\$4 near3 (physical near2 address) near4 (logical near2 address)) same ((varying various different differing) near5 memory) same ((varying various different differing) near5 address) same (controller BIST self-test self adj test)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/14 15:29
L4	21679	(714/710,711,718,720,733,734,738, 742,743 365/49,200,201,230.06 711/5,170,202,203,207,217).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/14 15:29
L2	0	L1 and (map\$4 near3 (physical near2 address) near4 (logical near2 address)) same ((varying various different differing) near5 memory) same ((varying various different differing) near5 address) same (controller BIST self-test self adj test)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/14 15:29
L1	19547	(714/710,711,718,720,733,734,738, 742,743 365/49,200,201,230.06 711/5,202,203,217).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/14 15:29
S50	2	(map\$4 near3 (physical near2 address) near4 (logical near2 address)) same ((varying various different differing) near5 memory) same ((varying various different differing) near5 address) same (controller BIST self-test self adj test)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/14 15:27
S39	18773	(714/710,711,718,720,733,734,738, 742,743 365/49,200,201,230.06 711/5,202,203,217).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/14 15:26
S49	3694	(map\$4 with (physical near2 address) with (logical near2 address)) same ((varying various different differing) near5 memory) ((varying various different differing) near5 address) same (controller BIST self-test self adj test)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/13 13:41

S48	4786	(map\$4 with (physical near2 address) with (logical near2 address)) same ((var\$4 differ\$4) near5 memory) ((var\$4 differ\$4) near5 address) same (controller BIST self-test self adj test)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/13 13:40
S47	9	map\$4 with (physical near2 address) with (logical near2 address) same ((plurality differ\$4) with memory) and (BIST or self-test or self adj test)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/13 13:37
S36	40	((map\$4 convert\$3) near5 (physical near4 (location address)) near5 (logical near4 (location address))) same memory) and (BIST or self-test or self adj test)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/13 12:29
S46	158	map\$4 with (physical near2 address) with (logical near2 address) same ((plurality differ\$4) with memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/13 12:28
S45	871	map\$4 with (physical near2 address) with (logical near2 address) and ((plurality differ\$4) with memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/13 12:28
S44	1455	map\$4 with (physical near2 address) with (logical near2 address)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/13 12:27
S43	6004	(physical near2 address) with (logical near2 address)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/13 12:27
S42	4	((crossbar cross-bar) same (physical near2 address) same (logical near2 address) and memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/13 12:26
S41	0	((crossbar cross-bar) near4 switch) same (physical near2 address) same (logical near2 address) and memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/13 12:24
S5	46	(map\$4 with physical near2 address same logical near2 address) and memory and (BIST or self-test or self adj test or test) same (controller scheduler)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/13 12:23

S40	16	S39 and (((map\$4 convert\$3) near5 (physical near4 (location address)) near5 (logical near4 (location address))) same memory) and (BIST or self-test or self adj test)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/11 15:03
S38	1803	(714/710,711,720,733,734).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/11 15:01
S20	558	(714/710,711).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/11 14:55
S37	2	"5854801".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/11 12:23
S35	1340	(((map\$4 convert\$3) near5 (physical near4 (location address)) near5 (logical near4 (location address))) same memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/08 13:18
S34	2071	((map\$4 convert\$3) same (physical near4 (location address)) same (logical near4 (location address)) same memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/08 13:16
S8	6	(((BIST or self-test or self adj test or test) near3 (controller scheduler)) same ((map\$4 with physical near4 address) same logical near4 address)) and memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/08 13:11
S33	1225	(retiming timing) near (interface circuit device unit) and ((address\$3 near4 memory) same (column and row))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/09 14:38
S32	7	((US-6629166-\$ or US-6525987-\$ or US-6469945-\$ or US-6236602-\$).did. or (US-20020176310-\$ or US-20010048625-\$ or US-20030167426-\$ or US-20030120985-\$ or US-20030167428-\$).did.) and ((address\$3 near4 memory) same (column and row))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/09 14:24

S31	9	(US-6629166-\$ or US-6525987-\$ or US-6469945-\$ or US-6236602-\$). did. or (US-20020176310-\$ or US-20010048625-\$ or US-20030167426-\$ or US-20030120985-\$ or US-20030167428-\$).did.	US-PGPUB; USPAT	OR	OFF	2004/04/09 13:53
S30	1	"5805792".PN.	US-PGPUB; USPAT	OR	ON	2004/04/08 16:09
S29	1	"6237123".PN.	US-PGPUB; USPAT	OR	ON	2004/04/08 16:04
S28	1	"5107501".PN.	US-PGPUB; USPAT	OR	ON	2004/04/08 16:00
S27	1	"5699288".PN.	US-PGPUB; USPAT	OR	ON	2004/04/08 15:59
S26	95	(BIST or built-in near2 self-test or built-in-self-test or self-test or self adj test or in adj circuit adj test) and ((physical coordinate "XY") and (logical or binary or bits) and ((714/733,734).ccls.))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/08 15:30
S24	92	(BIST or built-in near2 self-tes or self-test or self adj test) and ((physical coordinate "XY") and (logical or binary or bits) and ((714/733,734).ccls.))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/08 15:28
S25	2	(physical coordinate "XY") and (logical or binary or bits) and ((physical coordinate "XY") and (logical or binary or bits) and ((714/733,734).ccls.)) and ((714/710,711).ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/08 15:21
S23	197	(physical coordinate "XY") and (logical or binary or bits) and ((714/733,734).ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/08 15:21
S22	69642	(physical coordinate "XY") same (logical or binary or bits)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/08 15:19
S7	9187	(BIST or self-test or self adj test or test) near3 (controller scheduler)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/08 15:16

S21	43559	(physical coordinate "XY") with (logical or binary or bits)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/08 15:13
S19	889	(714/733,734).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/08 15:12
S18	648	(714/733,734.).ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/08 15:11
S17	883991	logical or binary or bits	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/08 15:10
S16	1096864	physical coordinate "XY"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/08 15:10
S15	0	(((BIST or self-test or self adj test or test) near3 (controller scheduler translator)) and ((map\$4 with physical near4 address) same logical near4 address same (row and column))) and memory ) not (((BIST or self-test or self adj test or test) near3 (controller scheduler translator)) and ((map\$4 with physical near4 (address location bit) same logical near4 (address location bit) same (row and column))) and memory )	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/08 15:09
S14	5	(((BIST or self-test or self adj test or test) near3 (controller scheduler translator)) and ((map\$4 with physical near4 (address location bit) same logical near4 (address location bit) same (row and column))) and memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/08 14:41
S13	5	(((BIST or self-test or self adj test or test) near3 (controller scheduler translator)) and ((map\$4 with physical near4 address) same logical near4 address same (row and column))) and memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/08 14:40

S12	9396	(BIST or self-test or self adj test or test) near3 (controller scheduler translator)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/08 14:30
S11	23	((controller scheduler) near6 generat\$3 near4 physical near2 address same logical near2 address) and memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/08 14:27
S10	370	(generat\$3 near4 physical near2 address same logical near2 address) and memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/08 13:57
S9	12	((BIST or self-test or self adj test or test) near3 (controller scheduler)) same ( physical near4 address same logical near4 address) and memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/08 13:56
S6	22	(map\$4 with physical near2 address same logical near2 address) and memory and (BIST or self-test or self adj test or test) near6 (controller scheduler)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/08 13:47
S4	1102	(map\$4 with physical nea2r address same logical near2 address) and memory and (BIST or self-test or self adj test or test) same (controller scheduler)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/08 12:57
S3	3982	(map\$4 with physical nea2r address same logical near2 address) and memory and (BIST or self-test or self adj test or test) same (control\$4 schedul\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/08 12:54
S2	7204	(map\$4 with physical nea2r address same logical near2 address) and memory and test	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/08 12:47
S1	13096	(map\$4 with physical nea2r address same logical adres) and memory and test	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/04/08 12:36